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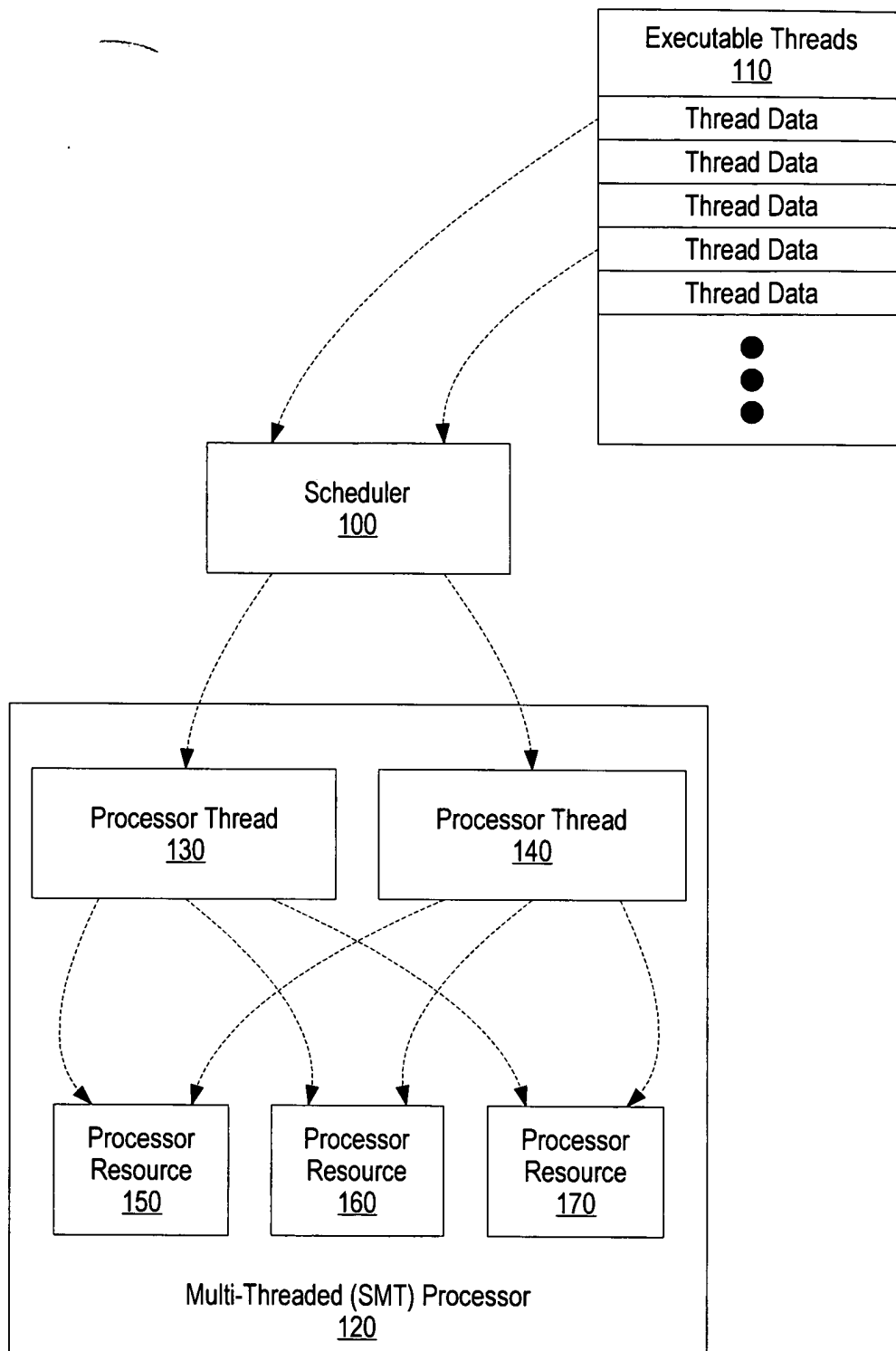


Figure 1

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Thread ID <u>215</u>	1 st Compatible Thread ID <u>220</u>	1 st Compatible Thread CPI <u>225</u>	2 nd Compatible Thread ID <u>230</u>	2 nd Compatible Thread CPI <u>235</u>	3 rd Compatible Thread ID <u>240</u>	3 rd Compatible Thread CPI <u>245</u>
aaa	bbb	0.7	ddd	1.0	fff	1.2
bbb	aaa	0.7	ccc	1.3	[null]	[null]
ccc	bbb	1.3	ddd	0.8	fff	1.5
ddd	aaa	1.0	ccc	0.8	ggg	0.9
eee	ddd	1.4	[null]	[null]	[null]	[null]
fff	aaa	1.2	ccc	1.5	[null]	[null]
ggg	ddd	0.9	[null]	[null]	[null]	[null]
hhh	[null]	[null]	[null]	[null]	[null]	[null]
Thread Control Block <u>210</u>						

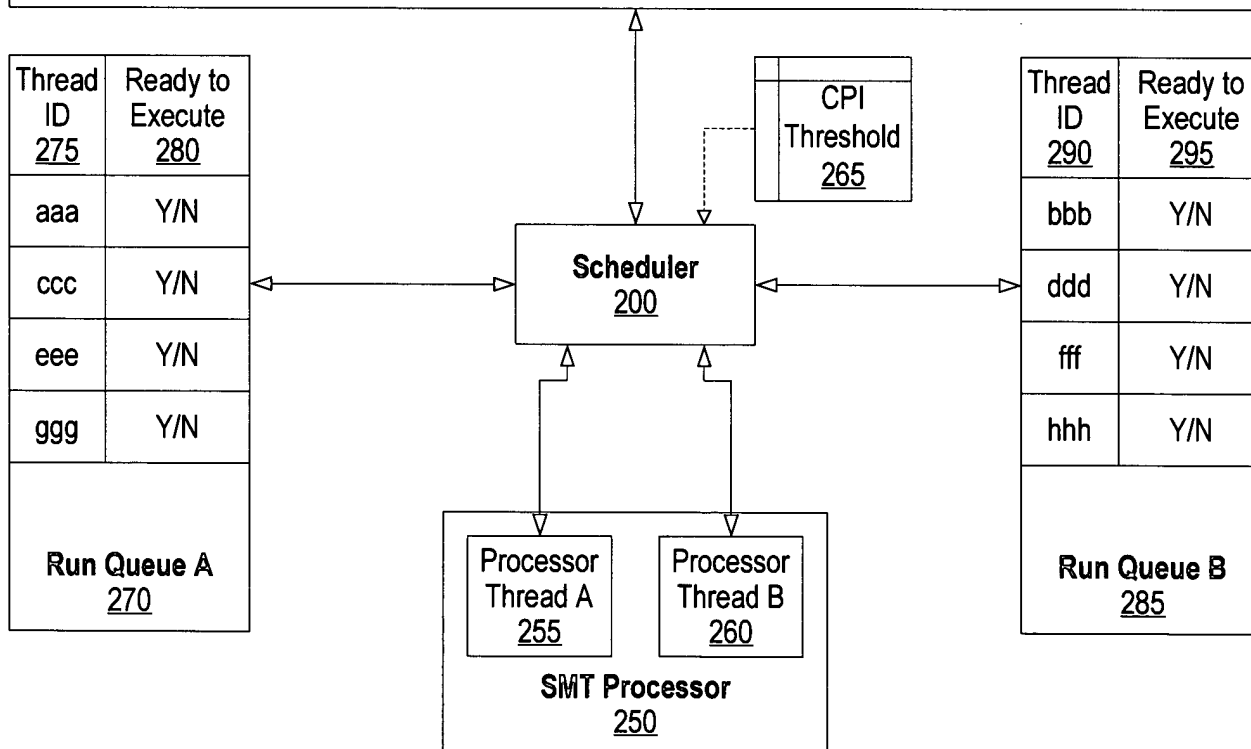
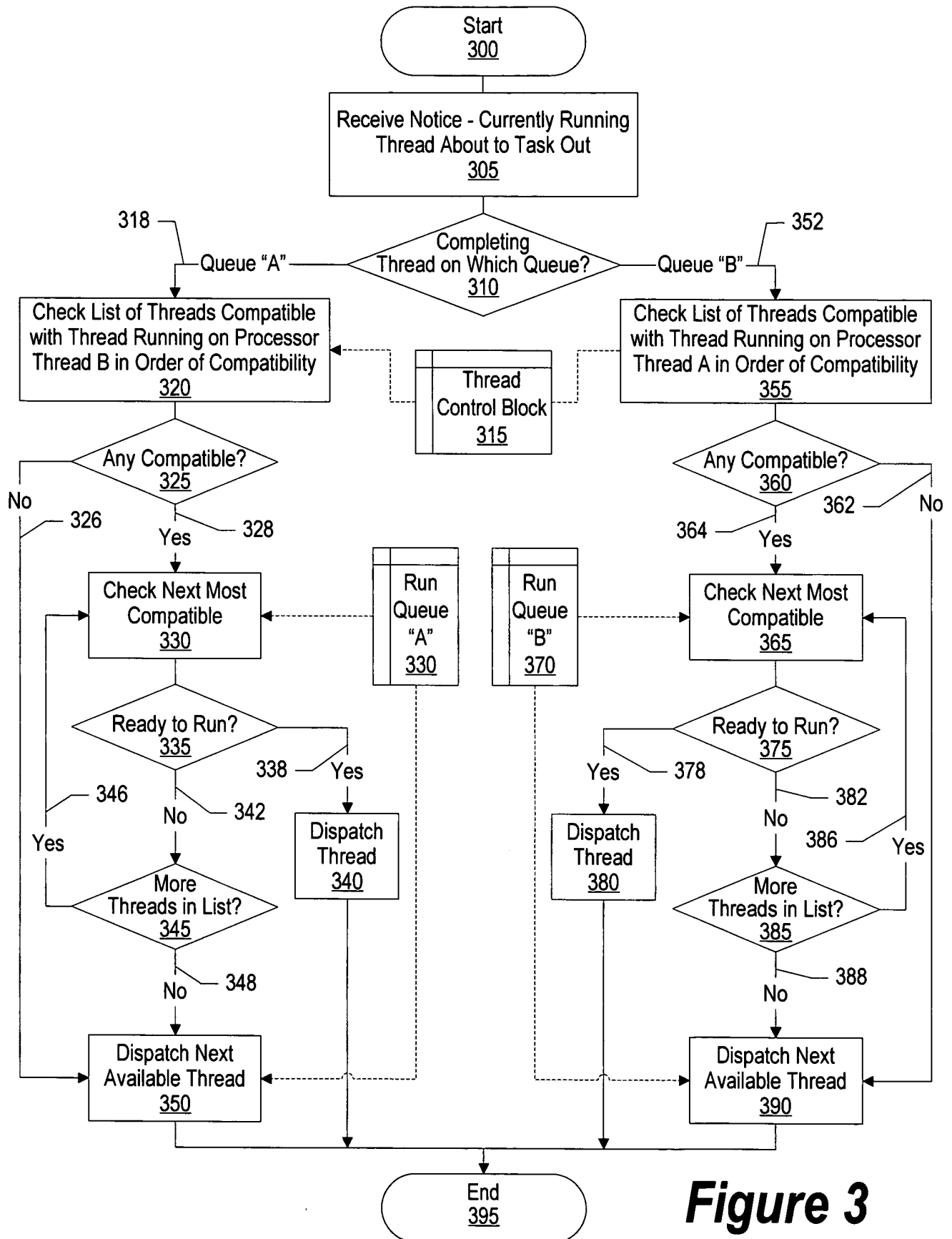


Figure 2

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**Figure 3**

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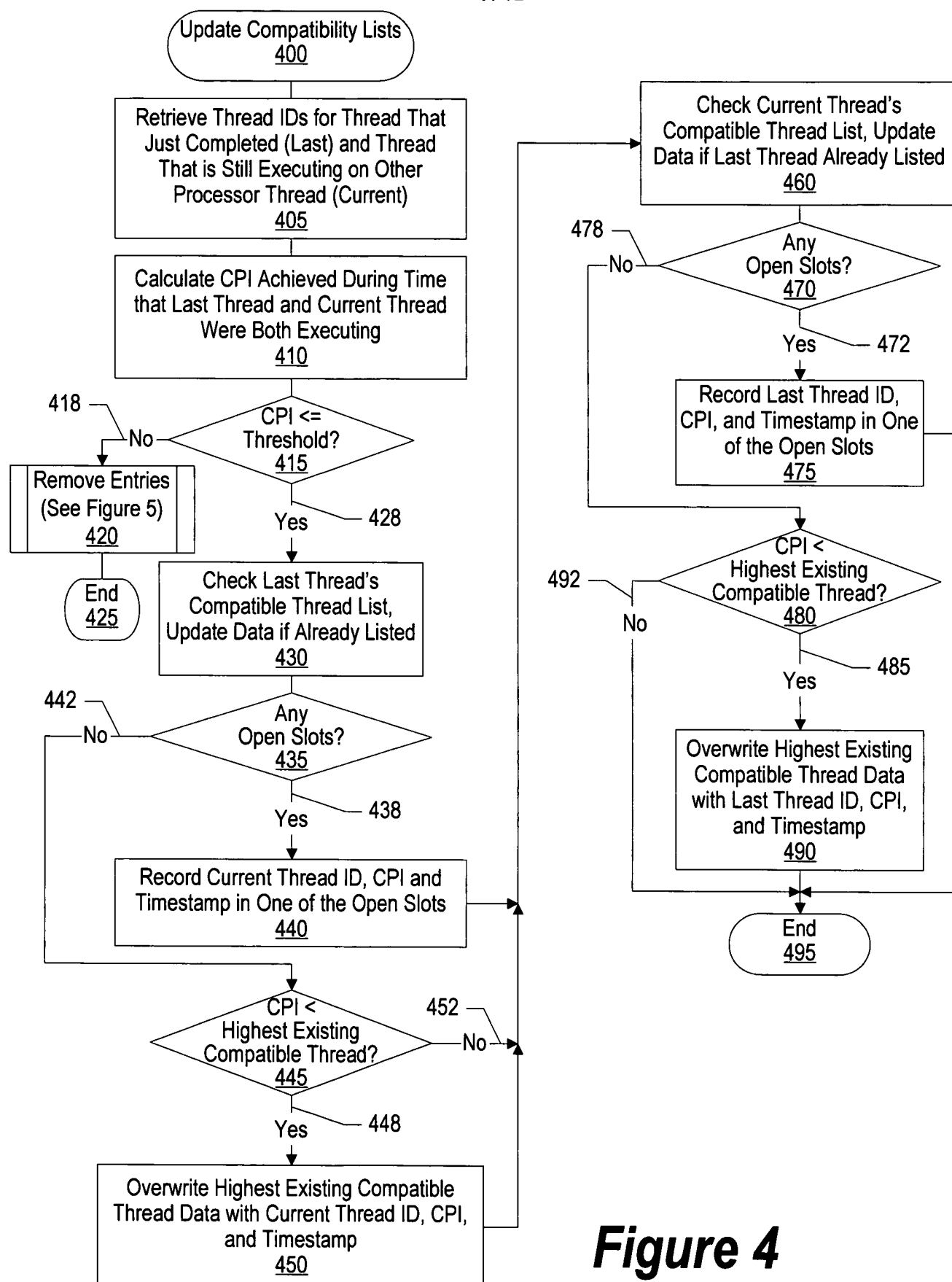
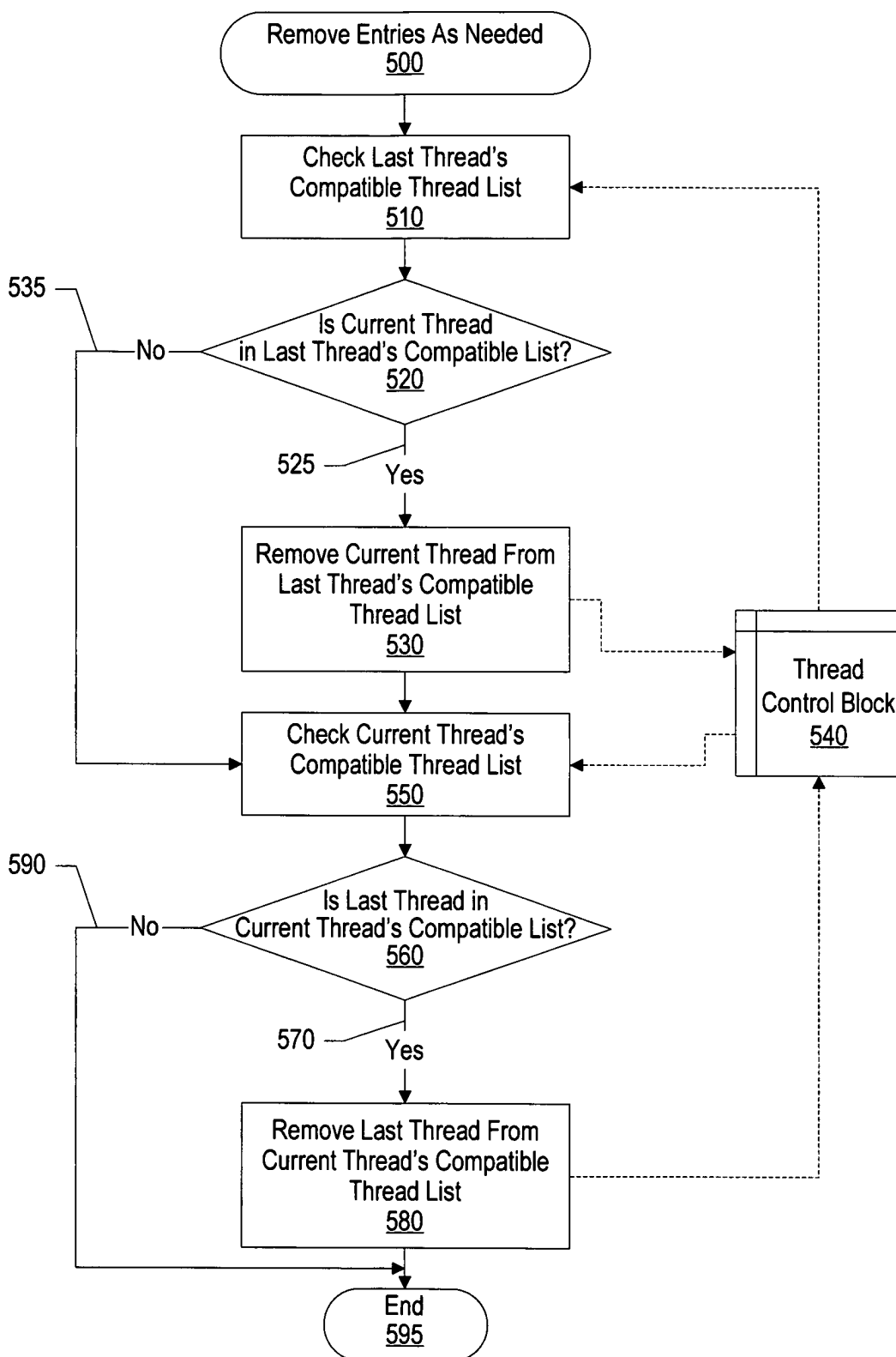
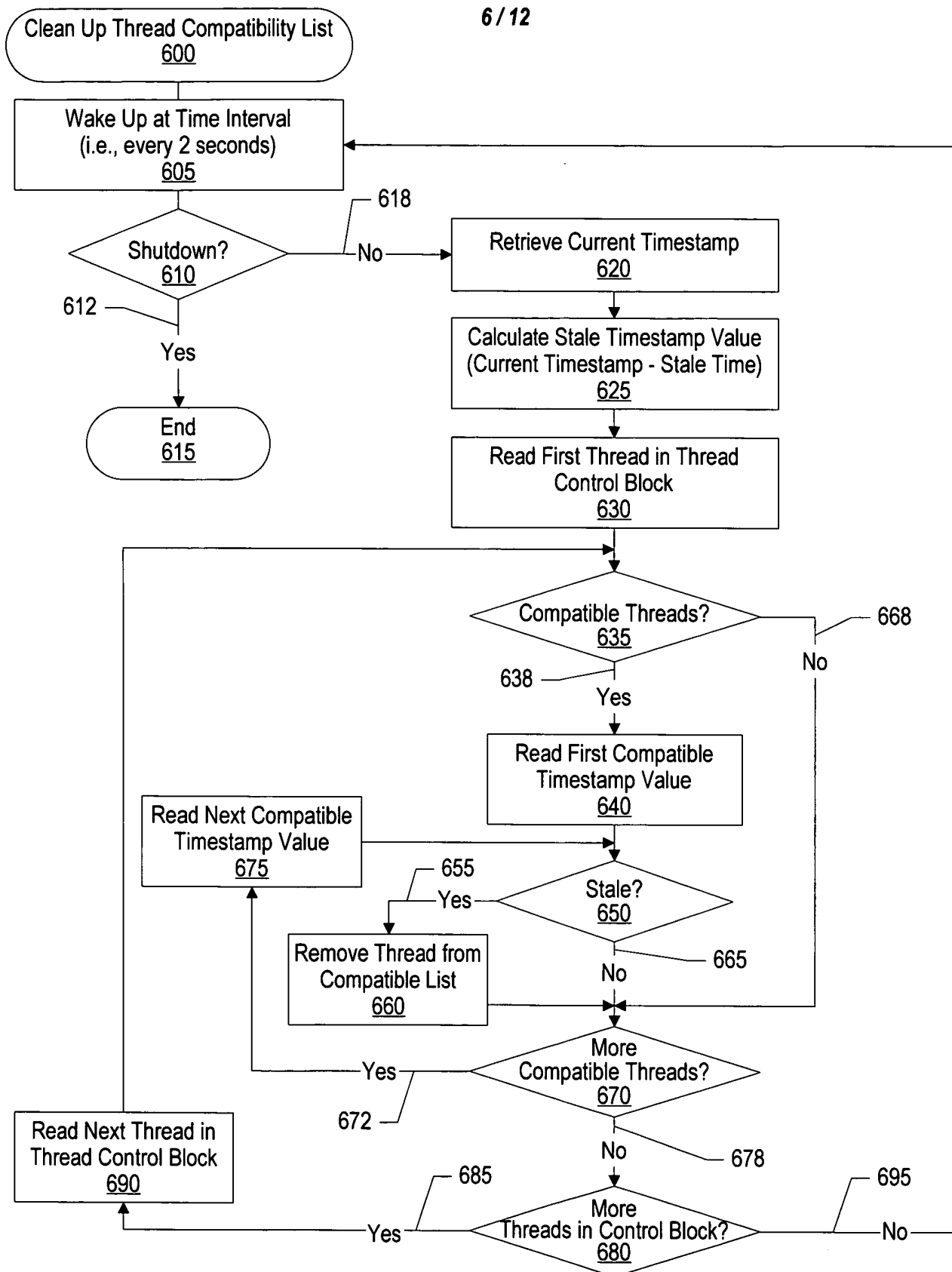


Figure 4

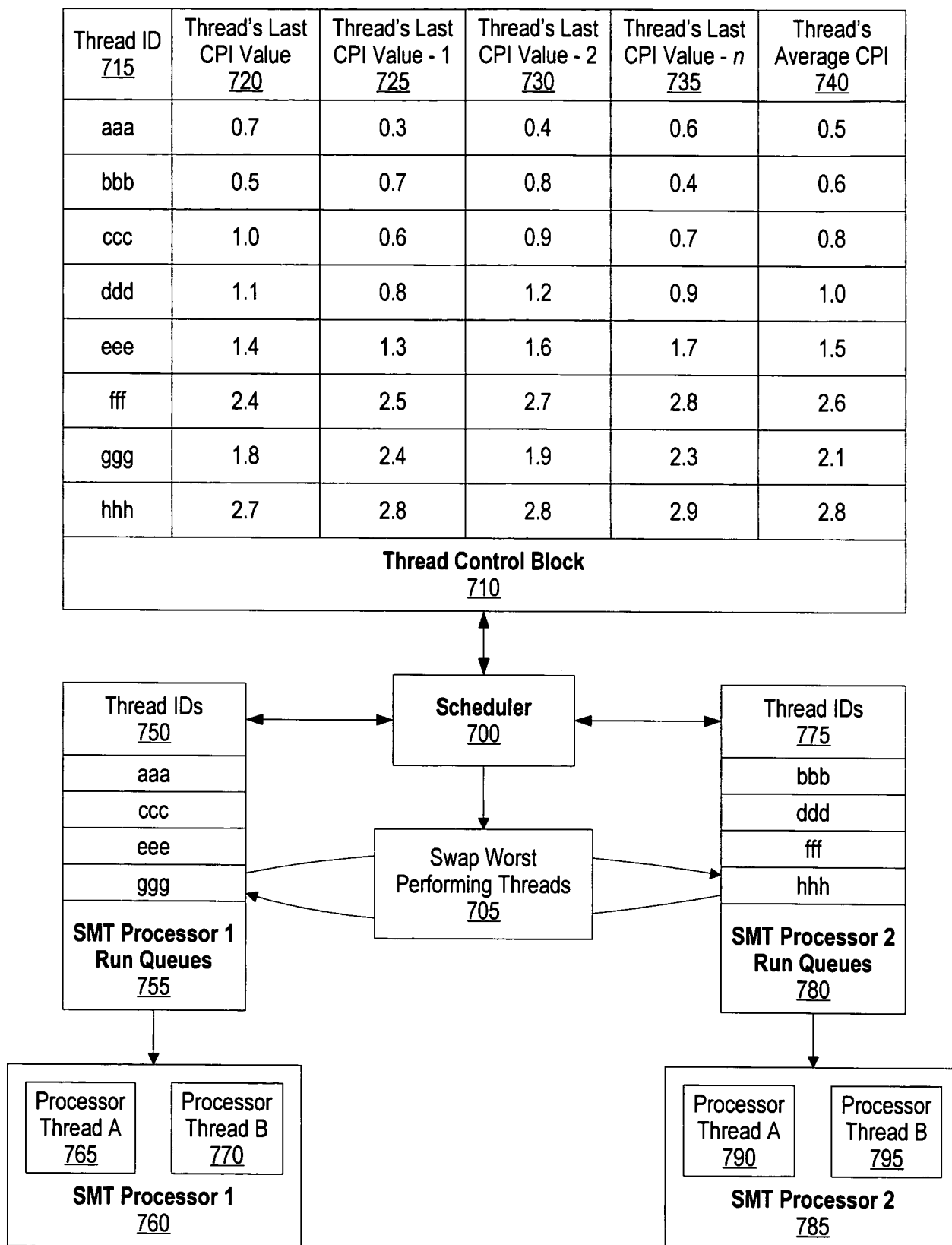
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**Figure 5**

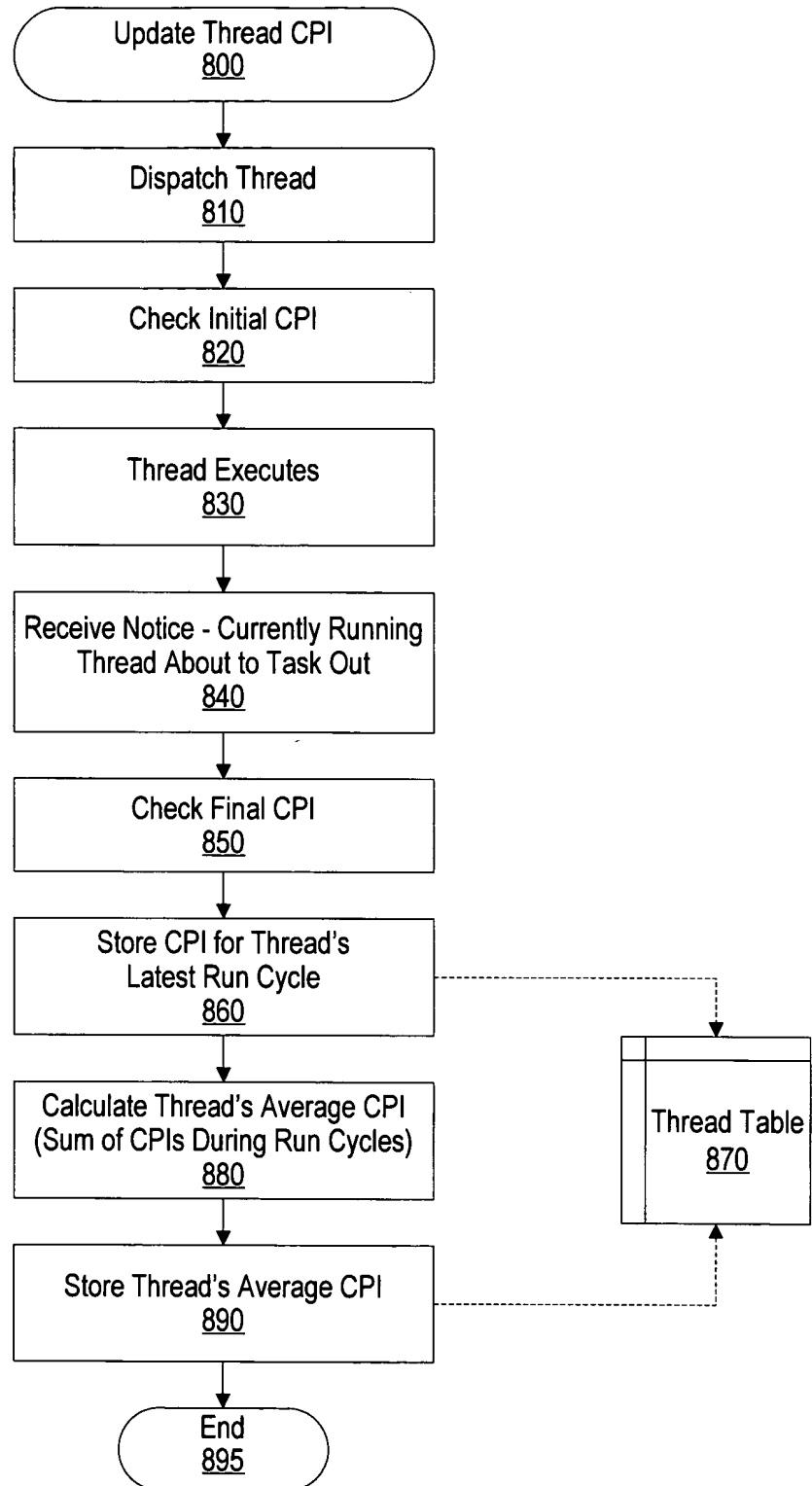
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**Figure 6**

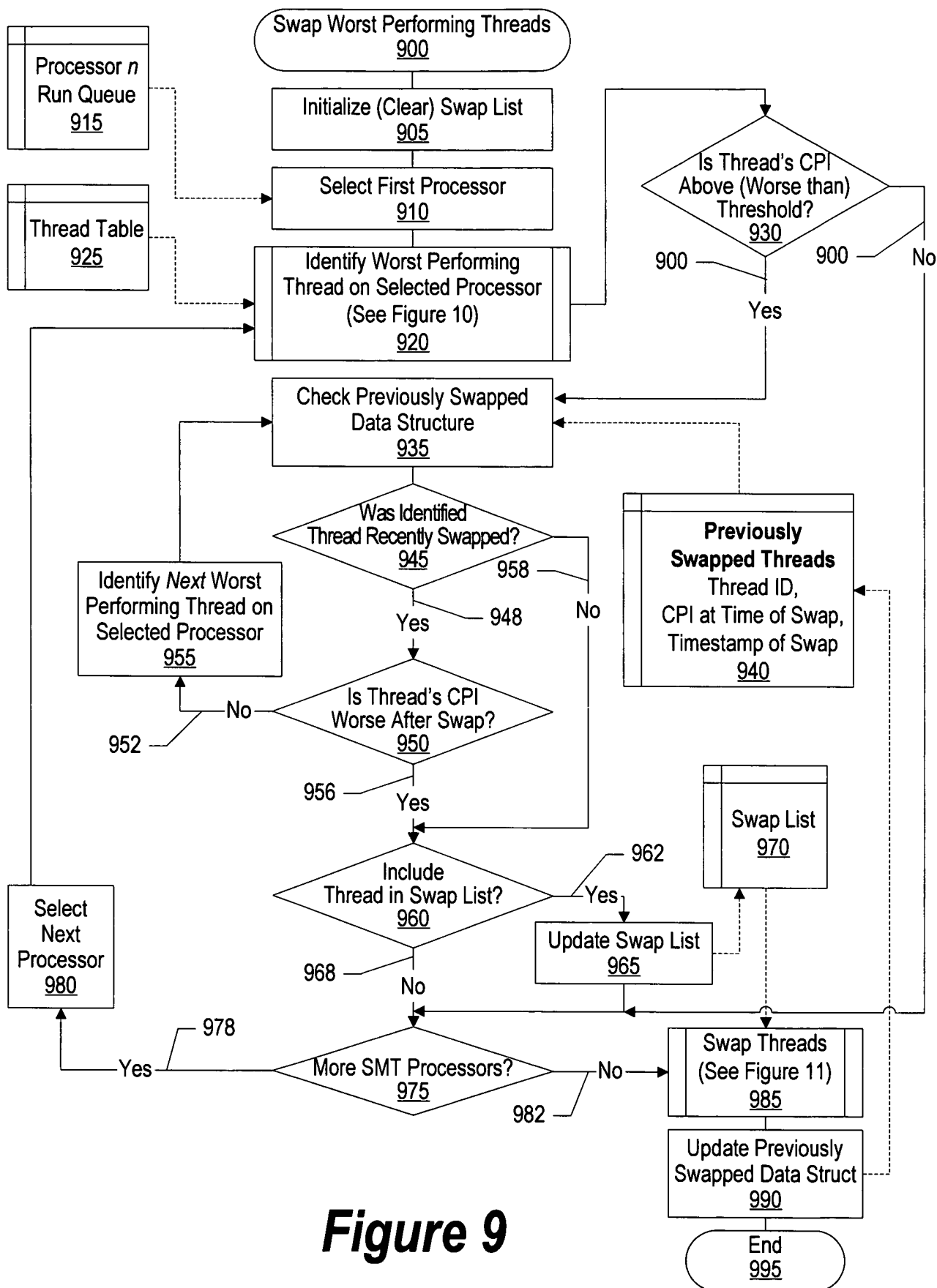
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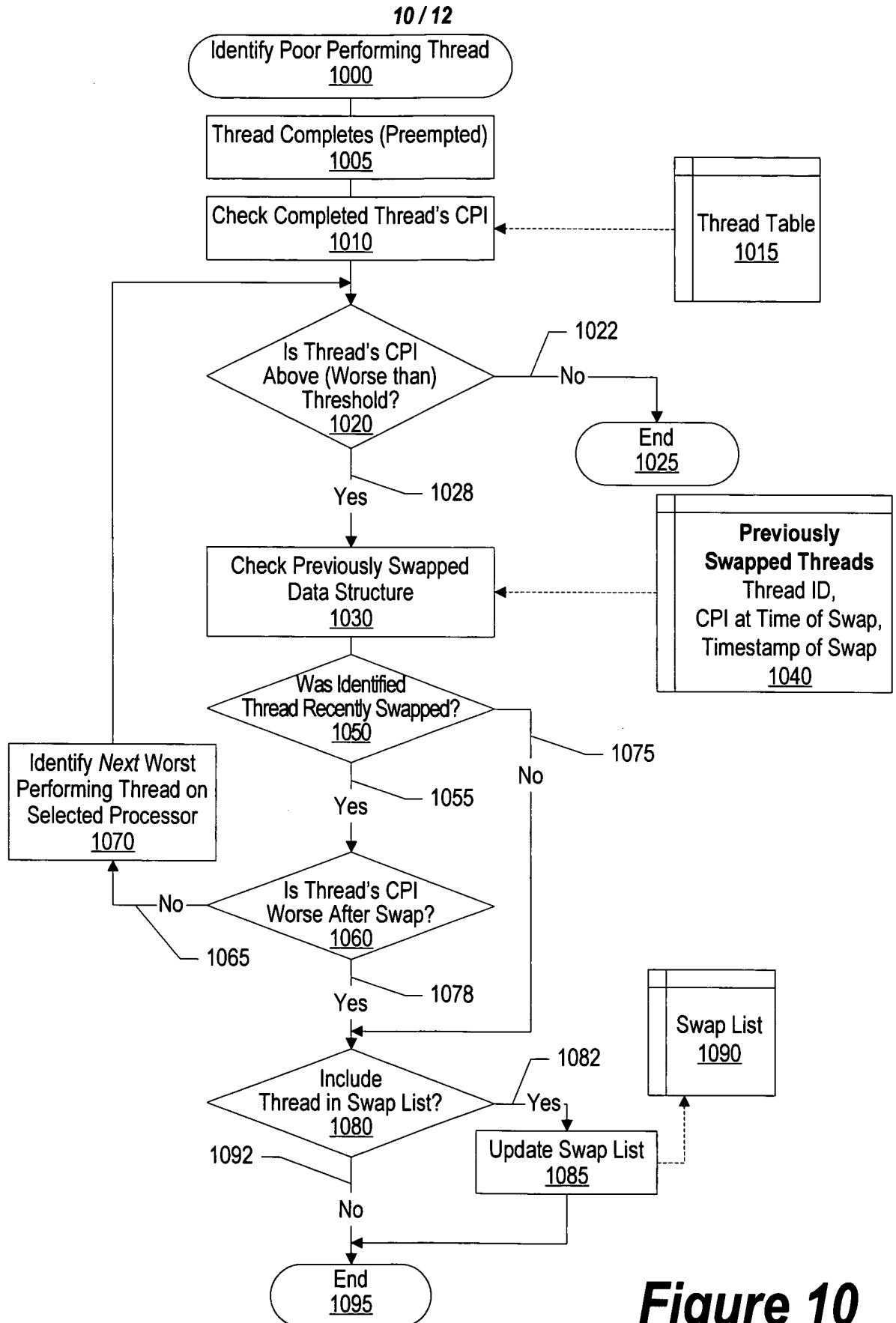
**Figure 7**

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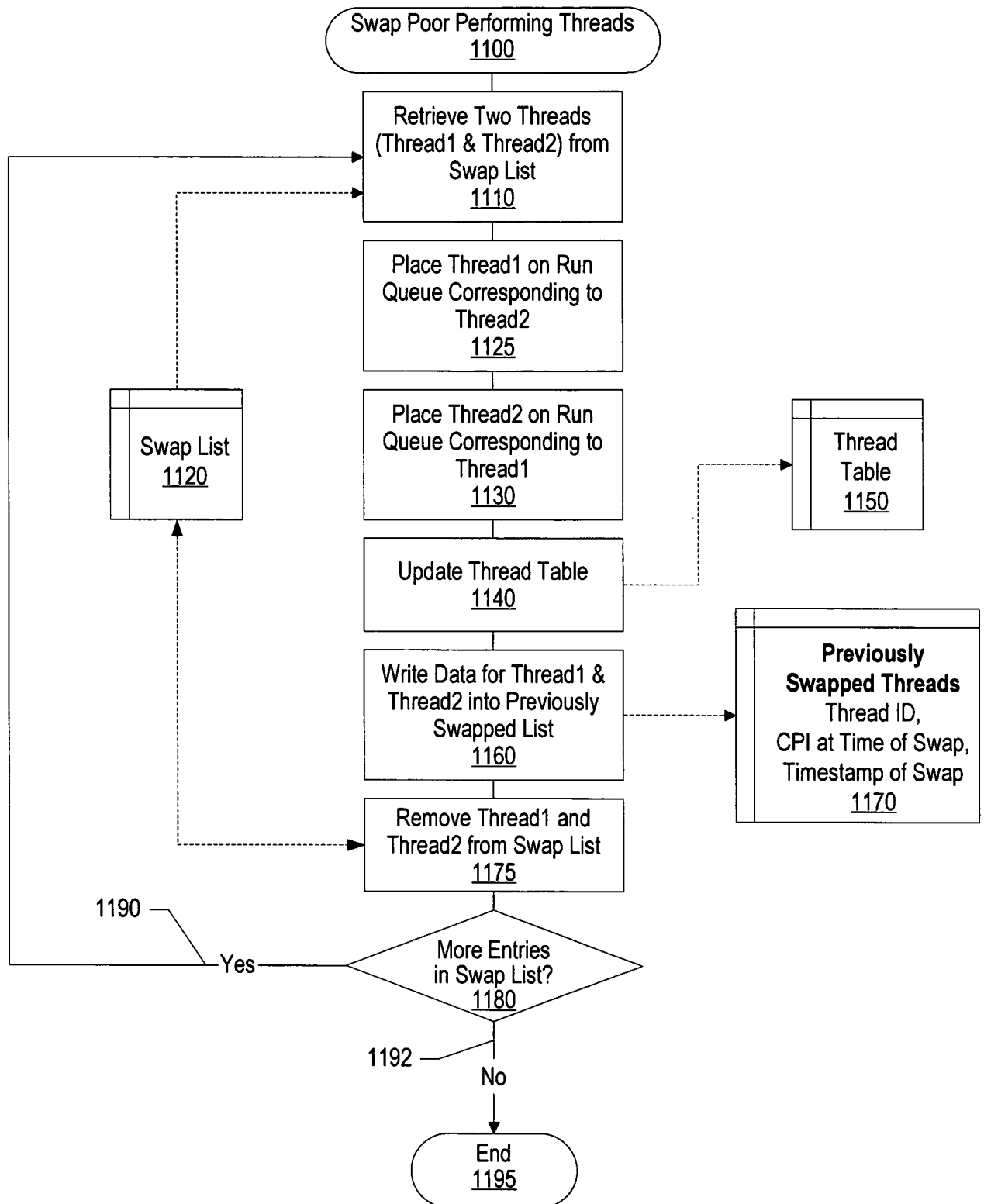
**Figure 8**

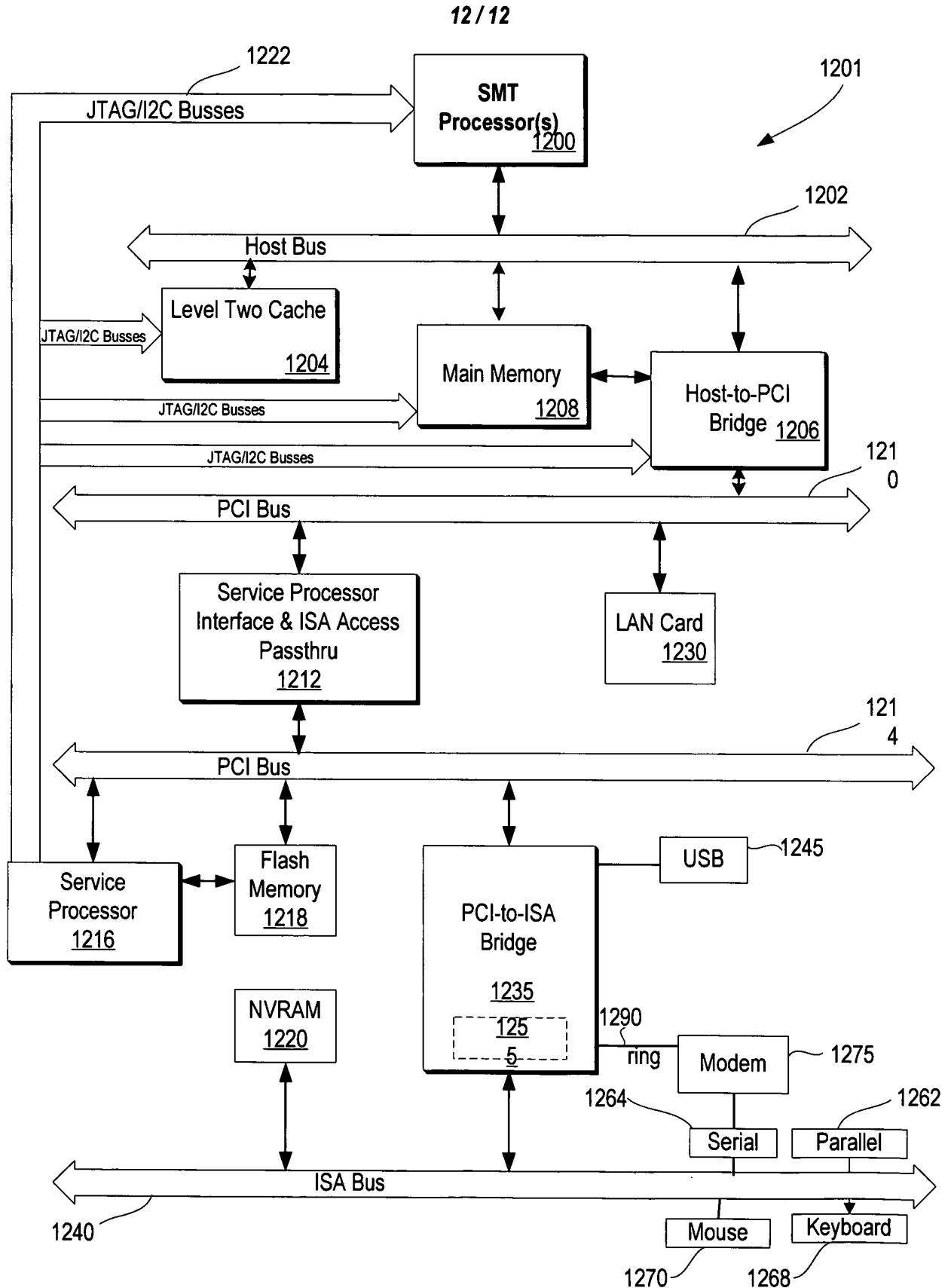
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**Figure 9**



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**Figure 11**

**Figure 12**